

**SPECIFICATION AMENDMENTS**

Please amend the specification on page 4, starting on line 26 as follows:

Figure 21 illustrates a schematic block diagram of another embodiment of a low power transmitter in accordance with the present invention; and

Figure 22 illustrates a logic diagram of a method for distributing processing within a low power transmitter; and

Figure 23 illustrates a schematic block diagram of a signal partitioning module and signal processing module in accordance with an alternative embodiment of the present invention.